

FIG. 2

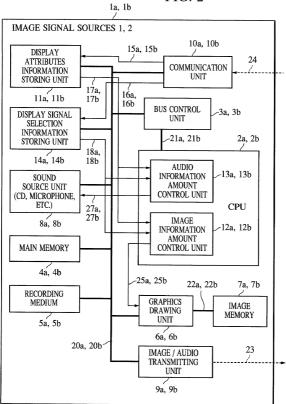


FIG. 3

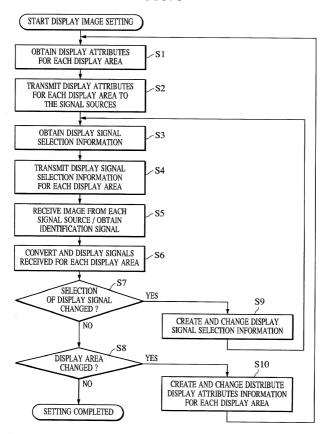


FIG 4

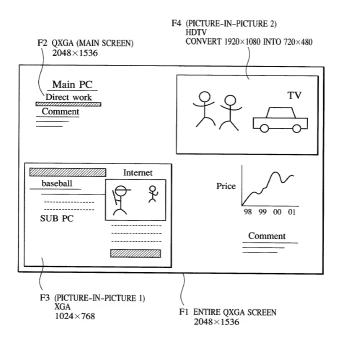
END OF DATA	H5 H5 c BYTES CHECKSIM
DISPLAY ATTRIBUTES INFORMATION	H4 d BYTES
DISPLAY AREA ID	H3 c BYTES
ORIGINATOR ADDRESS	H2 BYTES
RECIPIENT ADDRESS	\ H1 a BYTES

FIG. 5

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END OF DATA	H5 e BYTES CHECKSUN
IDENTIFICATION SIGNAL FOR	H6
APPROPRIATE INPUT SIGNALS	f BYTES
DISPLAY	H3
AREA ID	c BYTES
ORIGINATOR	H2
ADDRESS	b BYTES
RECIPIENT	H1
ADDRESS	a BYTES

FIG. 6

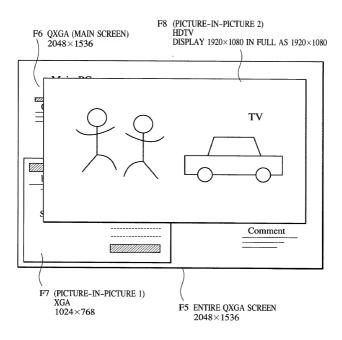


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FIG 7

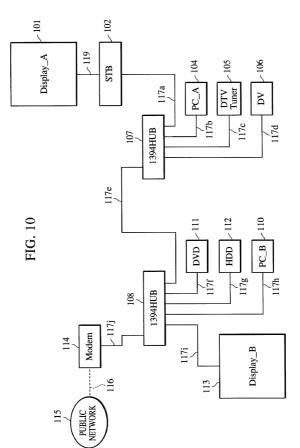
DISPLAY AREA	F2	F3	F4
PER-AREA DISPLAY	RESOLUTION: QXGA	RESOLUTION: XGA	RESOLUTION: SDTV
ATTRIBUTES	GRADIENTS: 8-BIT	GRADIENTS: 8-BIT	GRADIENTS: 8-BIT
ATTRIBUTED SIGNAL SOURCE	PC1	PC2	DVD
INPUT IDENTIFICATION	COMMUNICATION	IDENTIFICATION ID OF	COMMUNICATION
SIGNAL	ADDRESS FOR PC1	OUTPUT SIGNAL OF PC2	ADDRESS FOR DVD

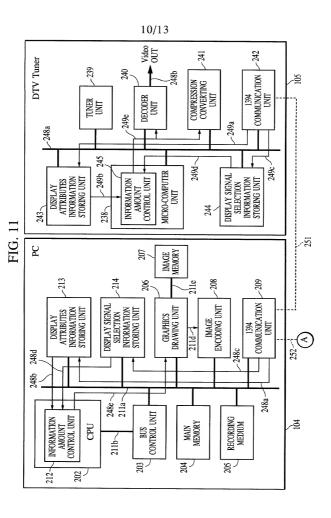
FIG. 8



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DISPLAY AREA	F2	F3	F4
PER-AREA DISPLAY	RESOLUTION: QXGA	RESOLUTION: XGA	RESOLUTION: HDTV
ATTRIBUTES	GRADIENTS: 8-BIT	GRADIENTS: 8-BIT	GRADIENTS: 8-BIT
ATTRIBUTED SIGNAL SOURCE	PC1	PC2	DVD
INPUT IDENTIFICATION	COMMUNICATION	IDENTIFICATION ID OF	COMMUNICATION
SIGNAL	ADDRESS FOR PC1	OUTPUT SIGNAL OF PC2	ADDRESS FOR DVD





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